

Notice of References Cited		Application/Control No. 10/541,465	Applicant(s)/Patent Under Reexamination MINO ET AL.	
		Examiner Ajay K. Arora	Art Unit 2811	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages
	U	Dallas Semiconductor MAXIM application note 653. Circuit Tradeoffs Minimize Noise in Battery-Input Power Supplies, 8 pages, Jan 22, 2001
	V	
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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